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16018 U.S. PTO  
10/758652



011604

13281 U.S. PTO  
011604  
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\*ADMITTED TO A BAR OTHER THAN VA.

**MS PATENT APPLICATION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Transmitted herewith for filing is the patent application of

Inventor(s): Tomohisa OKUNO

For: VOLTAGE CONVERSION CIRCUIT, SEMICONDUCTOR INTEGRATED CIRCUIT  
DEVICE, AND PORTABLE TERMINAL

Enclosed are:

- ☒ A specification consisting of fifty-four (54) pages
- ☒ Fourteen (14) sheet(s) formal drawings
- ☐ Applicant claims small entity status under 37 C.F.R. § 1.27
- ☐ Applicant does not claim priority
- ☒ Applicant claims the right of priority under the provisions of 35 U.S.C. § 119 and 37 C.F.R. § 1.55(a) based on Application No(s). 2003-11622 filed in Japan on January 20, 2003.
  - ☒ Certified copy(ies) is(are) attached hereto.
  - ☐ Certified copy(ies) will follow.
- ☒ Executed Declaration (☒ Original ☐ Photocopy)

- ☒ An assignment of the invention
- ☐ Application Data Sheet in accordance with 37 C.F.R. § 1.76
- ☐ Preliminary Amendment
- ☒ Information Disclosure Statement, PTO-1449 and reference(s)
- ☒ Other: Letter advising of co-pending application under provisions of MPEP § 2001.06(b).
- ☐ Applicant requests early publication - \$300.00 publication fee
- ☐ Non-publication Request and Certification under 35 U.S.C. § 122(b)(2)(B)(i)

The filing fee has been calculated as shown below:

|                                                                 |                 |                 | LARGE ENTITY  | SMALL ENTITY  |
|-----------------------------------------------------------------|-----------------|-----------------|---------------|---------------|
| BASIC FEE                                                       |                 |                 | \$770.00      | \$385.00      |
|                                                                 | NUMBER<br>FILED | NUMBER<br>EXTRA | RATE FEE      | RATE FEE      |
| TOTAL<br>CLAIMS                                                 | 20- 20 =        | 0               | X 18 = \$0.00 | x 9 = \$0.00  |
| INDEPENDENT<br>CLAIMS                                           | 2- 3 =          | 0               | x 86 = \$0.00 | x 43 = \$0.00 |
| <input type="checkbox"/> MULTIPLE DEPENDENT<br>CLAIMS PRESENTED |                 |                 | + \$290.00    | + \$145.00    |
| TOTAL                                                           |                 |                 | \$770.00      | \$0.00        |

- ☒ A check in the amount of \$810.00 to cover the filing fee and recording fee (if applicable) is enclosed.
- ☐ Please charge Deposit Account No. 02-2448 in the amount of \$0.00. A triplicate copy of this transmittal form is enclosed.

☒ Please send correspondence to:

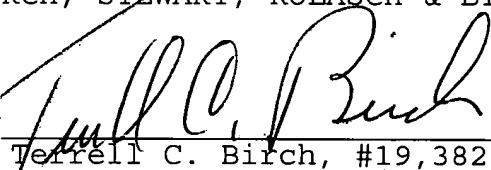
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If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By

  
Terrell C. Birch, #19,382

TCB/pjh  
1248-0696P

P.O. Box 747  
Falls Church, VA 22040-0747  
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Attachment(s)

(Rev. 11/30/03)

PATENT  
1248-0696P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Tomohisa OKUNO Conf.:  
Appl. No.: NEW Group:  
Filed: January 16, 2004 Examiner:  
For: VOLTAGE CONVERSION CIRCUIT,  
SEMICONDUCTOR INTEGRATED CIRCUIT  
DEVICE, AND PORTABLE TERMINAL

L E T T E R

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

January 16, 2004

Sir:

Under the provisions of MPEP § 2001.06(b), the Examiner is hereby advised of the following co-pending U.S. Application(s):

| <u>Appl. No.</u> | <u>Filing Date</u> | <u>Group</u> |
|------------------|--------------------|--------------|
| 10/470,125       | July 25, 2003      | 2819         |

The subject matter contained in the above-listed co-pending U.S. application(s) may be deemed to relate to the present application, and thus may be material to the prosecution of this instant application.

Copies of cited U.S. patent application(s) (specification, claims, and the drawings) or copies of the portion of the application which caused it to be cited, including any claims directed to that portion are attached hereto.

☒ The materials in the envelope are considered trade secrets and are being submitted for consideration under MPEP § 724.

The above-listed co-pending application(s) is(are) not to be construed as prior art. By bringing the above-listed application(s) to the attention of the Examiner, Applicant(s) do(does) NOT waive any confidentiality concerning the above-listed co-pending application(s) or this application. See MPEP § 101. Furthermore, if said application(s) should not mature into patents, such application(s) should be preserved in secrecy under the provisions of 35 U.S.C. § 122 and 37 C.F.R. § 1.14.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fee required under 37 C.F.R. §§ 1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By 

Terrell C. Birch, #19,382

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(Rev. 09/30/03)